

September 9, 2001 (because September 9<sup>th</sup> is a Sunday, this may be filed on Monday, September 10, 2001), is being filed concurrently.

In accordance with the rule changes, amendments to the claims are presented herein by presenting a complete set of pending claims, as amended, in clean form. Also, an Appendix entitled "Version With Markings to Show Changes Made," showing the current amendments to the claims is attached hereto.

Please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please replace the previous version of the claims with the following clean version, wherein claims 20, 31, 40, 43, 45, 49 and 50 incorporate new amendments thereto.

Sub II

20. (Seven Times Amended) A camera comprising:  
a camera body;  
an imaging device to conduct a photographing operation, wherein following a photographing operation said imaging device outputs image information;  
a first connection adapted to be connected to a first semiconductor memory;  
a second connection adapted to be connected to a second semiconductor memory;  
a buffer memory for temporarily storing image information so that the stored image information is transmitted to said second semiconductor memory from said buffer memory;  
a recorder which stores image information, output from said imaging device, on one of the first semiconductor memory and the second semiconductor memory;  
a detector to detect a memory condition; and  
a changer, coupled to said detector, to selectively change between a first condition, in which image information outputted from said imaging device is directed to the first connection for storage on a connected first semiconductor memory, and a second condition, in which image information outputted from said imaging device is directed to the second connection for storage on a connected second semiconductor memory based on a detected condition by said detector.

21. A camera according to Claim 20, wherein the first semiconductor memory is detachably mountable to the camera body, and the second semiconductor memory is fixedly provided in the camera body.

22. A camera according to Claim 20, wherein the first semiconductor memory is an IC card.

31. (Four Times Amended) An editing device for use with a memory card having a relatively small capacity and for storing image information taken by a camera, in which the camera processes original image information obtained in a photographing operation to convert to digital image information and to compress for storage in the memory card and further stores such compressed image information on the memory card, the editing device comprising:

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a first reception unit to receive a removable memory card;  
a second reception unit to receive a memory device having a relatively large capacity;  
a signal processor to decompress the compressed digital image information, stored on a memory card removably received by the first reception unit, to a decompressed digital information; and  
a recorder to record the decompressed digital image information on the memory device.

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32. An editing device according to Claim 31, wherein the signal processor restores the processed image information by expansion in a DCT manner.

33. A camera according to Claim 20, which further comprises a finder for finding a camera subject.

34. A camera according to Claim 20, which further comprises a printer for printing on a recording medium corresponding to the image information stored on one of the first semiconductor memory and the second semiconductor memory.

37. An editing device according to Claim 31, which further comprises a reading device to read image information from the memory device.

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40. (Six Times Amended) A camera comprising:  
a camera body;  
an imaging device to conduct a photographing operation, wherein following a photographing operation said imaging device outputs image information;  
a first connection adapted to be connected to a first memory;  
a second connection adapted to be connected to a second memory;  
a recording device to store image information on one of the first memory and the second memory;  
a detector to detect an available memory capacity and to output a signal representative of a result of such a detection; and

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a buffer memory for temporarily storing image information so that the stored image information is directed to said second memory from said buffer memory;

a first changer to selectively change between a first condition, in which image information outputted from said imaging device is directed to the first connection for storage in the first memory, and a second condition, in which image information outputted from said imaging device is directed to the second memory;

a reproduction device to receive and reproduce image information stored on and outputted from one of the first memory and the second memory; and

a second changer to select one of the first memory and the second memory to provide image information to the reproduction device for reproduction.

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41. A camera according to Claim 40, wherein said first and second memories are semiconductor memories.

42. A camera according to Claim 40, which further comprises a finder for finding a camera subject.

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43. (Five Times Amended) A camera comprising:  
a camera body;  
an imaging device to conduct a photographing operation, wherein following a photographing operation said imaging device outputs image information;  
a first SRAM memory capable of storing image information corresponding to at least two photographic frames;  
a second SRAM memory, wherein at least one of said first SRAM memory and the second SRAM memory is provided in the camera body;  
a buffer memory for temporarily storing image information so that the stored image information is transmitted to said second semiconductor memory from said buffer memory;  
a recording device provided within the camera body for selectively storing image information on one of the first SRAM memory and the second SRAM memory;  
a detector to detect a condition of one of the first SRAM memory or the second SRAM memory; and

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a changer, provided within the camera body, for causing said recording device to selectively change from a first condition, in which image information outputted from said imaging device is stored on the first SRAM memory, and a second condition, in which image information outputted from said imaging device is stored on the second SRAM memory based on a detected condition of one of the first SRAM memory and the second SRAM memory.

44. A camera according to Claim 43, wherein said first SRAM memory is contained on an IC card which is detachably mountable to the camera body, and wherein said second SRAM memory is provided in the camera body.

45. (Three Times Amended) A camera according to Claim 43, further comprising:

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a reproduction device to reproduce image information stored on a selected one of the first SRAM memory and the second SRAM memory; and

a changer provided within the camera body to cause said reproduction device to selectively change between a third condition, in which image information stored on said first SRAM memory is outputted from said first SRAM memory to the reproduction device for reproduction, and a fourth condition, in which image information stored on said second SRAM memory is outputted from said second SRAM memory to the reproduction device for reproduction.

46. A camera according to Claim 45, wherein said reproduction device is provided within said camera body.

47. A camera according to Claim 20, wherein the memory condition concerns whether a first semiconductor memory is connected to the first connection.

48. A camera according to Claim 20, wherein the memory condition concerns whether a connected first semiconductor memory maintains a sufficient memory capacity.

Sub Ia

49. (Once Amended) A camera comprising:  
a camera body;  
an imaging device to conduct a photographing operation, wherein following a photographing operation said imaging device outputs image information;  
a first connection adapted to be connected to a first semiconductor memory;  
a second connection adapted to be connected to a second semiconductor memory;  
a buffer memory for temporarily storing image information so that the stored image information is transmitted to said second semiconductor memory from said buffer memory;  
a recorder which stores image information, output from said imaging device, on one of the first semiconductor memory and the second semiconductor memory;  
a detector to detect a memory condition; and  
an alarm mechanism to alert a user of a detected memory condition.

50. (Once Amended) A camera according to Claim 49, wherein the selected status concerns memory capacity, and the alarm mechanism alerts the user whether an available memory capacity is below a threshold value.

51. A method to edit image information in an editing device for a camera capable of receiving a memory card, in which the camera processes original image information obtained in a photographing operation in a manner suitable for storage and further stores such processed image information on the memory card, wherein the editing device includes a first reception unit to receive a removable memory card and a second reception unit to receive a memory device, the method comprising the steps of:

receiving image information from a memory card received within the first reception unit;

restoring received image information to original image information originally obtained in a photographing operation; and

recording restored image information on a memory device received within the second reception unit.